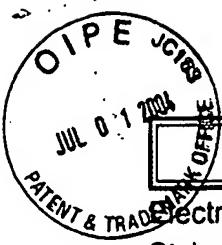


<b>FORM PTO-1449 U.S. Department of Commerce Patent and Trademark Office</b>  <b>LIST OF DOCUMENTS CITED BY APPLICANT</b>  (Use several sheets if necessary)				Attorney Docket Number 5649-1100IP			Serial No. To Be Assigned <i>10815505</i>
				Applicants: Min-sang Park et al.			
				Filing Date: Concurrently Herewith			Group
<b>U. S. PATENT DOCUMENTS</b>							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<i>JRW</i>	1	5,931,927	8/3/99	Takashima	710	65	
<b>FOREIGN PATENT DOCUMENTS</b>							
		Document Number	Date	Country	Class	Subclass	Translation Yes   No
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							

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 \*EXAMINER

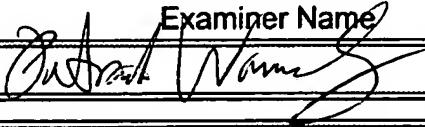
DATE CONSIDERED

Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

Title of Invention	Integrated Circuit Devices Having Data Inversion Circuits Therein with Multi-Bit Prefetch Structures and Methods of Operating Same						
Application Number:	10/815505 						
Confirmation Number:	1295						
First Named Applicant:	Min-sang Park						
Attorney Docket Number:	5649-1100IP						
Art Unit:	2819						
Search string:	( 6731567 or 6456551 or 5477551 or 4462102 ).pn.						
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
✓	1	6731567	2004-05-04	Acharya	B2	365	233
✓	2	6456551	2002-09-24	Sohn	B2	365	219
✓	3	5477551	1995-12-19	Parks		371	37.7
✓	4	4462102	1984-07-24	Povlick		371	49
Signature							
Examiner Name 				Date 05/09/2005			